



9/Amdt 2823
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2/1/02
PATENT APPLICATION
ATTORNEY DOCKET NO. Q56320

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re application of

Kaoru NARITA

Appln. No.: 09/421,273

Confirmation No.: Not Yet Assigned

Group Art Unit: 2823

Filed: October 20, 1999

Examiner: J. Garcia

For: SEMICONDUCTOR DEVICE HAVING PROTECTION CIRCUIT IMPLEMENTED BY
BIPOLAR TRANSISTOR FOR DISCHARGING STATIC CHARGE CURRENT AND
PROCESS OF FABRICATION

AMENDMENT UNDER 37 C.F.R. § 1.111

Commissioner for Patents
Washington, D.C. 20231

Sir:

In response to the Office Action dated November 23, 2001, please amend the above-identified application as follows:

IN THE CLAIMS:

Please enter the following amended claims:

- Sub B1
1. (Amended) A semiconductor integrated circuit device provided on a semiconductor substrate of one conductivity type, comprising:
- shallow trench isolating regions having a first depth, and provided in surface portions of said semiconductor substrate for defining active areas therebetween;
- a terminal provided on said semiconductor substrate;

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